

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :
Chung-Hung LIN et al. : Group Art Unit: Unassigned
Serial No.: Unassigned : Examiner: Unassigned
Filed: November 26, 2003 :

For: INTEGRATED CIRCUIT PACKAGING FOR IMPROVING EFFECTIVE CHIP-BONDING AREA

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
BOX PATENT APPLICATION
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR 1.56, and 37 CFR 1.97-1.98, the documents listed on the attached form PTO-1449 are hereby made of record in this patent application. Copies of the listed documents are enclosed.

As this Information Disclosure Statement is being filed prior to the mailing of the first Official Action in this application, no fee is believed due in order to have the enclosed reference considered by the Examiner and made of record in the application.

Early action on the merits of the application is earnestly solicited.

Respectfully submitted,

By:


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Date: November 26, 2003

FORM PTO 1449 (*modified*)ATTY DOCKET NO. **BHT-3183-61**APPLICATION NO. **Unassigned**U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
*(Use several sheets if necessary)*Date Submitted to PTO: **November 26, 2003**APPLICANT **Chung-Hung LIN et al.**FILING DATE **November 26, 2003**GROUP **Unassigned****U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT
		TW20000125608	9-21-01	Taiwan, R.O.C.			

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED